

JC997 U.S. PTO
09/998131

12/03/01

438	301	Subclass
Class	ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
FA SCANNED AC3 Q.A. SS	

APPLICATION NO. 09/998131	CONT/PRIOR D F	CLASS 438	SUBCLASS 301	ART UNIT 2825	EXAMINER B. KRISHNAN
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APPLICANTS
Ji Park
Dong Sohn

TITLE
Semiconductor device having a gate and fabrication method therefor

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)								
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
438	301	438	197	200	585	592	630	631		
INTERNATIONAL CLASSIFICATION			646	647						
H01L	21/336									

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 4	Figs. Drwg. 10	Print Fig. 26	Total Claims 21	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	B. Krishnan (Assistant Examiner)			11/22/01 (Date)	
	_____ (Primary Examiner)			_____ (Date)	
	_____ (Legal Instruments Examiner)			_____ (Date)	

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due Date Paid

ISSUE BATCH NUMBER

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